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- (71) Applicant (for DE only): PHILIPS INTELLECTUAL PROPERTY & STANDARDS GMBH [DE/DE]; Steindamm 94, 20099 Hamburg (DE).
- (71) Applicant (for all designated States except DE, US): KONINKLIJKE PHILIPS ELECTRONICS N. V. [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).
- (72) Inventor; and
- (75) Inventor/Applicant (for US only): SCHOLZE, Steffen [DE/DE]; c/o Philips Intellectual Property & Standards GmbH, Weisshausstr. 2, 52066 Aachen (DE).

- (74) Agents: VOLMER, Georg et al.; Philips Intellectual Property & Standards GmbH, Weisshausstr. 2, 52066 Aachen (DE).
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(54) Title: USE-AUTHORIZATION DEVICE FOR SECURITY-RELATED APPLICATIONS

key unit	interface	lock unft
Initialization		Inttialization
Compute support points s() from v _p N _r G and b (as appropriate only for positive I), set C = N-1 Set intermediate values z(I) ← s(I) Compute v _p Compute v _p	×	0. Send v ₀ = X and N (optional) 1. Compute v _N 2. Set C = C-1
Authentication		Authentication
0. Receive X (optional) 1. Serid Y = V c 2. Receive Z (optional) 3. Set C = C-1 4. Updates s(i) and z(i) 5. Compute V c as next output vehae	Y Z	O. Send X (optional) I. Rooche Y C. Compare Y with intermediately computed v _C J. Send Z (optional) 4. Set C = C-1

(57) Abstract: This description is given of a use-authorization device for security-related applications, in particular access control to secure areas or securing vehicles with a useroperated key unit for generating consecutive, alternating user code information which exhibits a sequence of consecutive function values $v_{i+1} = F(v_b \ const)$ for i=0,...,N through the repeated use of a one-way function $F(v_b \ const)$, which function values are used in inverse order to the sequence formation to create the consecutive user code information, and an application-sided processing unit for determining actual authorization information which is dependent upon the user code information received from the key unit and for performing a use-authorization checking process by comparing this actual authorization information with target authorization information, as well as for generating use-release information depending on the result of the comparison, wherein the target authorization information has a function value v; which has been transferred from the user code information processed during the previous positive use-authorization operation. The special feature of the invention is that there is a certain number of levels G provided, with at least one support point and one intermediate value, from which a certain number of iterative function value calculations can be performed in each level by means of the one-way function $F(v_b \ const)$ wherein there are G = L(N) / b levels, with N as the starting value, L(N) as the number of bits required for representing N in the dual system and D as the basis.

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